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09/856,926	05/29/2001	Kazuhiho Tsuda	55884/70904	6384

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EXAMINER	
LEWIS, DAVID LEE	

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/856,926	Applicant(s) TSUDA ET AL.	
	Examiner David L Lewis	Art Unit 2673	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *19 April 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-66* is/are pending in the application.

4a) Of the above claim(s) *1-26* is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *27-66* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *5 & 6*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Election/Restrictions

A. **Claims 1-26 are withdrawn** from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected Groups I and II, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 4/19/2004. A complete reply to the final rejection must include cancellation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

1. **Claim 27-41, 44, 45, and 47-66 is rejected under 35 U.S.C. 102(e) as being anticipated by Yamazaki (6522319).**

2. **As in claim 27, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1, column 15 lines 50-65**, and

supplying through a data signal line a data signal to a corresponding pixel of the scanning signal line as selected, **figure 3 items X and Y, column 18 lines 47-67, column 19 lines 7-14**, wherein a quiescent period, **figure 3 item VC or (50H-10H), column 19 liens 1-3**, in which all the scanning signal lines are set in non-scanning state, is set to be longer than a scanning period required for scanning the screen one time, **figure 3 item VC (40H) > 10H**, **column 18 lines 45-55**, wherein a sum of the scanning period and the quiescent period is set to be equivalent to one vertical period, **figure 3 item 50H(f1), column 19 lines 1-5.**

3. **As in claim 28, Yamazaki teaches of**, wherein: a non-scanning period including the quiescent period is selected among a plurality of non-scanning periods, figure **column 18 lines 60-67**.

4. **As in claim 29, Yamazaki teaches of**, satisfying the condition of: (T1 + T02) = (T1 +TO1) x N (N is an integer of not less than 2), wherein Ti is the scanning period, TO1 is the shortest one of the plurality of said non-scanning periods, and T02 is a non-scanning period other than TO1, **column 39 lines 1-35**, wherein said nonselection value changes from zero to T less than one frame, while the scanning period is the same.

5. **As in claim 30, Yamazaki teaches of**, said display device includes image data storage means for storing image data based on which the data signal is produced, figure 5 item 14, column 21 lines 45-67, and an operation of transferring the image data from said image data storage means is stopped in the quiescent period, figure 6, column 21 lines 45-67.

6. **As in claim 31, Yamazaki teaches of**, wherein: said display device includes image data supply means for supplying image data based on which the data signal is produced, figure 5 item 15, and an operation of receiving a supply of the image data from said image data supply means is stopped in the quiescent period, figure 6, column 21 lines 45-67.

7. **As in claim 32, Yamazaki teaches of**, wherein: an operation of an analog circuit irrelevant to display is stopped in said quiescent period, figure 13, column 31 lines 47-53.

8. **As in claim 33, Yamazaki teaches of**, wherein: an operation of at least an analog circuit of said data signal line driver is stopped in the quiescent period, figure 11 item 26-29, column 29 lines 9-21.

9. **As in claim 34, Yamazaki teaches of**, wherein: said data signal lines are all set in high-impedance state with respect to at least said data signal driver for driving all of said data signal lines in the quiescent period, column 39 lines 29-42.

10. **As in claim 35, Yamazaki teaches of**, wherein: in said quiescent period, after setting said data signal lines all in high-impedance state, an operation of an analog circuit irrelevant to display is stopped, column 39 lines 29-42 and figure 13, column 31 lines 47-53.

11. **As in claim 36, Yamazaki teaches of**, wherein: in said quiescent period, an operation of at least an analog circuit of said data signal driver is stopped, figure 11 item 26-29, column 29 lines 9-21.

12. **As in claim 37, Yamazaki teaches of**, wherein: said data signal lines are all set in high impedance state after setting them to have potential at which variation in data holding state of all the pixels are minimized on average, column 18 lines 65-67, column 39 lines 27-42.

13. **As in claim 38, Yamazaki teaches of** a display device, comprising: control means for executing said method of driving a display device of claim 27, figure 1 item 5.

14. **As in claim 39, Yamazaki teaches of** an electronic device adopting the display device of claim 38, figure 24, column 40 lines 30-45.

15. **As in claim 40, Yamazaki teaches of**, wherein: said display device is a liquid crystal display device which includes a liquid crystal display element having pixels arranged in a matrix, figure 1 item 1:LCD, form in which a charge based on a data signal supplied through the data signal line is written periodically in an electric capacitance formed by interposing liquid crystal between a pixel electrode and a counter electrode via the active element as selected by a scanning signal to be supplied from the scanning signal line, figure 22, column 32 lines 40-67.

16. **As in claim 41, Yamazaki teaches of**, wherein: a non-selective voltage which substantially maximizes an OFF resistance value of the active element is applied to all of said scanning signal lines in the quiescent period, figure 3 item VC.

17. **As in claim 44, Yamazaki teaches of** a display device comprising: control means for executing the driving method of a display device of claim 40, figure 1 item 5.

18. **As in claim 45, Yamazaki teaches of**, wherein: said liquid crystal display element includes an auxiliary capacitance electrode which forms an auxiliary capacitance of the pixel with said pixel electrode, is formed so as not to be overlapped with said scanning signal lines, figure 22, column 32 lines 40-67, wherein said variation is known.

19. **As in claim 47, Yamazaki teaches of**, wherein: said liquid crystal display element includes a reflective member which realizes a reflective-type display using surrounding light, column 41 lines 5-20.

20. **As in claim 48, Yamazaki teaches of**, wherein: said reflective member constitutes at least a part of said pixel electrode, column 41 lines 5-20.

21. **As in claim 49, Yamazaki teaches of**, wherein: said reflective member either has a hole for transmitting therethrough light or is semi transmissive, column 41 lines 5-20.

22. **As in claim 50, Yamazaki teaches of** an electronic device adopting said display device of claim 44, figure 24, column 40 lines 30-45.

23. **As in claim 51, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1 item 1, column 15 lines 50-65 and figure 22, column 32 lines 40-65**, and supplying through a data signal line

a data signal to a corresponding pixel of the scanning signal line as selected, **figure 11, column 27 lines 60-67**, wherein: subsequent to a scanning period required for scanning a screen one time, a quiescent period, in which all the scanning signal lines are set in non-scanning state, is formed so as to be longer than the scanning period, and in said quiescent period, a potential of said data signal line is set to a predetermined data signal line quiescent potential, **figure 3 item VC**, wherein said quiescent period VC is 40H and said scanning period is 10H, **column 18 lines 40-57**.

24. **As in claim 52, Yamazaki teaches of**, wherein: the data signal line quiescent potential of said data signal line in the quiescent period is set within a range of a voltage of the data signal to be supplied to the data signal line in said scanning period, figure 3 item Xn, column 17 lines 16-25.

25. **As in claim 53, Yamazaki teaches of**, wherein: the data signal line quiescent potential of said data signal line in a quiescent period is set to a center of an amplitude of a data signal to be supplied to said data signal line in said scanning period, figure 3 item Xn, column 17 lines 16-25.

26. **As in claim 54, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1 item 1, column 15 lines 50-65 and figure 22, column 32 lines 40-65,** and supplying through a data signal line a data signal to a corresponding pixel of the scanning signal line as selected, **figure 11, column 27 lines 60-67,** wherein: a quiescent period, figure 3 item VC,

in which all the scanning signal lines are set in non-scanning state, is set to be longer than a scanning period required for scanning a screen one time, **figure 3 item VC**, wherein said quiescent period VC is 40H and said scanning period is 10H, **column 18 lines 40-57**, and in the quiescent period, a potential of a counter electrode is set to a predetermined counter electrode quiescent potential, **figure 5 item CNT, column 22 lines 10-30**.

27. **As in claim 55, Yamazaki teaches of**, wherein: the counter electrode quiescent potential of said counter electrode in the quiescent period is set within a range of a voltage of a counter electrode driving signal to be supplied to said counter electrode in the scanning period, figure 5 item CNT (hi/low), column 22 lines 10-30.

28. **As in claim 56, Yamazaki teaches of**, wherein: the counter electrode quiescent potential of said counter electrode in the quiescent period is set to a center of an amplitude of the counter electrode driving signal to be supplied to said counter electrode in the scanning period, column 22 lines 10-43.

29. **As in claim 57, Yamazaki teaches of** the method of driving a display device, wherein: a potential of said data signal line in said quiescent period is fixed to the data signal line quiescent potential, column 18 lines 65-67, and a potential of the counter electrode in said quiescent period is set to a counter electrode quiescent potential, column 22 lines 10-37.

30. **As in claim 58, Yamazaki teaches of** wherein: in said quiescent period, the potential of the data signal line and the potential of the counter electrode are set

to the data signal line quiescent potential, and the counter electrode quiescent potential respectively, column 18 lines 65-67, column 22 lines 10-37, and subsequently, said data signal line is set in high-impedance state with respect to said data signal driver for supplying data signals to said data signal lines, column 39 lines 27-40.

31. **As in claim 59, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1 item 1, column 15 lines 50-65 and figure 22, column 32 lines 40-65,** and supplying through a data signal line a data signal to a corresponding pixel of the scanning signal line as selected, **figure 11, column 27 lines 60-67,** wherein: subsequent to a scanning period required for scanning a screen one time, a quiescent period, in which all the scanning signal lines are set in non-scanning state, is formed so as to be longer than the scanning period, **figure 3 item VC**, wherein said quiescent period VC is 40H and said scanning period is 10H, **column 18 lines 40-57,** and an AC driving signal, having a frequency of not more than that of the data signal to be supplied to the data signal line in the scanning period, is applied to the data signal line in the quiescent period, **figure 3 item M, column 19 lines 4-13**.

32. **As in claim 60, Yamazaki teaches of** wherein: an amplitude of a driving signal to be applied to the data signal line in said quiescent period is set within a range of a voltage of a data signal to be supplied to the data signal line in the scanning period, figure 3, column 17 lines 16-67.

33. **As in claim 61, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1 item 1, column 15 lines 50-65 and figure 22, column 32 lines 40-65,** and supplying through a data signal line a data signal to a corresponding pixel of the scanning signal line as selected, **figure 11, column 27 lines 60-67,** wherein: subsequent to a scanning period required for scanning a screen one time, a quiescent period, in which all the scanning signal lines are set in non-scanning state, is formed so as to be longer than the scanning period, **figure 3 item VC**, wherein said quiescent period VC is 40H and said scanning period is 10H, **column 18 lines 40-57,** and an AC driving signal, **column 27 lines 50-60, figure 3 item M**, which is within a range of a voltage of a counter electrode driving signal to be supplied to said counter electrode in the scanning period and which has a frequency of not more than that of the counter electrode driving signal, is applied to the counter electrode in the quiescent period, **figure 3 item M, column 20 lines 20-35, column 27 lines 50-60**.

34. **As in claim 62, Yamazaki teaches of** the method of driving the display device, wherein: an AC driving signal is applied to the data signal line in the quiescent period, figure 6 item CA, an AC driving signal is applied to the counter electrode in the quiescent period, figure 6 item CNT, and both of said driving signals have identical frequencies and phases, column 22 lines 1-33.

35. **As in claim 63, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1 item 1, column 15 lines 50-65 and figure 22, column 32 lines 40-65,** and supplying through a data signal line a data signal to a corresponding pixel of the scanning signal line as selected, **figure 11, column 27 lines 60-67,** wherein: a quiescent period, in which all the scanning signal lines are set in non-scanning state, is set to be longer than a scanning period required for scanning a screen one time, **figure 3 item VC**, wherein said quiescent period VC is 40H and said scanning period is 10H, **column 18 lines 40-57,** and an AC driving signal, which is within a range of a voltage of a counter electrode driving signal to be supplied to said counter electrode in the scanning period and which has a frequency of not more than that of the counter electrode driving signal, is applied to the said counter electrode and said data signal line in the quiescent period, **figure 6 item PD, column 22 lines 1-30.**.

36. **As in claim 64, Yamazaki teaches of** a method of driving a display device which displays by selecting and scanning each scanning signal line of a screen having pixels arranged in a matrix form, **figure 1 item 1, column 15 lines 50-65 and figure 22, column 32 lines 40-65,** and supplying through a data signal line a data signal to a corresponding pixel of the scanning signal line as selected, **figure 11, column 27 lines 60-67,** wherein: subsequent to a scanning period required for scanning a screen one time, a quiescent period, in which all the

scanning signal lines are set in non-scanning state, is formed so as to be longer than the scanning period, **figure 3 item VC**, wherein said quiescent period VC is 40H and said scanning period is 10H, **column 18 lines 40-57,** and a DC driving signal, **column 27 lines 50-59**, having a potential within a range of a voltage of a counter electrode driving signal to be applied to said counter electrode in the scanning period, is applied to said counter electrode and the data signal line in the quiescent period, **figure 5 item CNT, column 22 lines 10-30**.

37. **As in claim 65, Yamazaki teaches of** a display device comprising control means which executes the driving method of the display device, figure 1 item 5, column 16 lines 15-25.

38. **As in claim 66, Yamazaki teaches of** an electronic device mounting the display device, figure 24, column 40 lines 30-45.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

39. Claims 42, 43, and 46 rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki (6522319).

40. **As in claims 42 and 43**, Yamazaki fails to explicitly teach of said specific range however said values represent those know to the skilled artisan at the time of the invention and would have been an obvious design choice given the time sequential AC driving method as taught by Yamazaki, column 18 lines 28-67, wherein driving voltage waveforms of the liquid crystal apparatus include known frame rate control.

41. **As in claim 46**, while Yamazaki fails to teach of said formulated claims language, said ratios would have been obvious to the skilled artisan at the time of the invention given Yamazaki's provided drive technique comprising said drive circuit shown in figure 11, including a hold means.

Conclusion

42. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. 2002/0175887, 2002/0027544, 6208083, 6072453, 6288496.

43. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is **(703) 306-3026**. The examiner can normally be reached on MT and THF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on (703) 305-4938. Any

inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231
or faxed to:
(703) 872-9314 (for Technology Center 2600 only)
Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).
Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.